

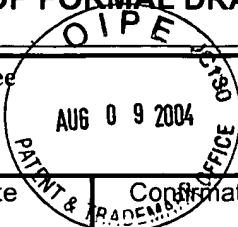
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TRANSMITTAL OF FORMAL DRAWINGS

Docket No.

Yee 6

In Re Application Of: **Oceager P. Yee**



Serial No.

10/748,065

Filing Date

12/29/2003

Confirmation No.

Examiner

Art Unit

Invention: **SYSTEM AND METHOD FOR DEBUGGING SYSTEM-ON-CHIPS**

Address to:

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

Transmitted herewith are:

5 sheets of formal drawing(s) for this application.

☒ Each sheet of drawing indicates the identifying indicia suggested in 37 CFR Section 1.84(c).

Signature

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Dated: 8/5/04

I certify that this document and attached formal drawings are being deposited on 8/5/04 with the U.S. Postal Service as first class mail under 37 C.F.R. 1.8 and addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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